ROCKWELL SEMICONDUCTOR SYSTEMS

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October 8, 1998

Bt471/8, Bt475/7, and Bt481A

Product Affected: Bt471/8, Bt475/7 and the Bt481A

product bulletin

DELIVERING

TERTAINMENT

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INFORMATION AND

Specification Differences Between the Bt471/8, Bt475/7 and the Bt481A.

Customers interested in replacing their existing designs using the Bt471KPJ80, Bt478KPJ80 or the Bt475KPJ80, Bt477KPJ80/110 with the Bt481AKPJ110, should consider the following specification differences. The Bt481A (110MHz 256-Word Color Palette 15-, 16-, and 24-bit True Color Power-Down RAMDAC) is form, fit, and functionally backwards compatible. Due to incompatibilities between the Bt475/7 and the Bt481A command registers, software modifications are necessary.

The Bt481A will replace the Bt471/8 and Bt475/7 with the following minor specification differences:

- The output will be delayed by 3 clock cycles.
- The output compliance is 0.2 V more positive.
- The Write Data Hold Time is 7 ns longer.

Please refer to Table 1 for detailed specification differences.

Specification	Bt471/8	Bt475/7	Bt481A	Comments
Output Compliance	-0.5 V	-0.5 V	-0.3 V	Sync More Positive
Pipeline	4	4	7	Should Not be an Issue Output Delayed 3 Clocks Should Not be an Issue
Sense Levels	N/A	395 mv (high)	430 mv (high)	Should Not be an Issue
		325 mv (low)	310 mv (low)	
RSO-2 Setup Time	10 ns	3 ns	10 ns	Should Not be an Issue
RSO-2 Hold Time	10 ns	3 ns	10 ns	Should Not be an Issue
Read Asserted to Databus				
Driven	5 ns	5 ns	2 ns	Should Not be an Issue
Read Data Hold Time	5 ns	5 ns	2 ns	Should Not be an Issue
Write Data Hold Time	3 ns	3 ns	10 ns	Should Not be an Issue

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Table 1

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Become Communication Matters D471XBL1

